an Open Access Journal by MDPI

Impact Factor 1.8
CiteScore 4.3

Journal of Low Power Electronics and Applications

mdpi.com/
journal/
jlpea

MDPI

Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications is an open access journal which provides an advanced forum for rapid dissemination of innovative research and important results in all aspects of low power electronics and design.

It publishes reviews, regular research papers and short communications. Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. The full experimental details must be provided so that the results can be reproduced.

Editor-in-Chief
Dr. Davide Bertozzi

Aims and Scope

Journal of Low Power Electronics and Applications (ISSN 2079-9268) is an open access journal which provides an advanced forum for rapid dissemination of innovative research and important results in all aspects of low power electronics and design.

The scope of the journal encompasses a broad range of topics including emerging devices and process technologies, analog, digital and mixed-signals VLSI circuits, architecture and system-level designs, SoCs and embedded systems, harvesting and battery-less systems, synthesis and optimization tools, and CAD tools and methodologies for low-power designs.

It publishes reviews, regular research papers and short communications. Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. There is no restriction on the maximum length of the papers. The full experimental details must be provided so that the results can be reproduced.

Subject Areas

Devices and Technologies
- Advanced technology nodes for low-power; emerging nanomaterials and nanotechnologies; sensors; energy storage devices.

Circuits
- Low-power digital circuits for logic; low-power analog/mixed-signal circuits; ultra-low-power RF circuits; ultra-low-power boost converter.

Architectures
- Low-power microarchitecture design; asynchronous design; System-on-Chip designs; embedded systems; approximate, brain-inspired and other non-conventional computing; HW/SW co-design; low-power architecture for image, video and graphic processing; signal processing; low-power FPGA architecture design; embedded FPGA applications; SoC FPGA for low-power; low-power deep learning architectures.

CAD tools and methodologies
- CAD tools and methodologies for low-power and thermal-aware design; tools for power estimation; power-aware synthesis and optimization; dynamic power management; power-gating techniques and design tools; energy-efficient software design and software for low-power applications in heterogeneous systems; runtime systems and toolchains.

Systems and applications
- Wearable computing; circuits and systems for Internet-of-Things (IoT); deep-learning low-power systems; implantable electronics; bio-sensor circuits; battery-less applications; Cyber-Physical Systems.

Author Benefits

Open Access

Unlimited and free access for readers

No Copyright Constraints

Retain copyright of your work and free use of your article

Thorough Peer-Review

Discounts on Article Processing Charges (APC)

If you belong to an institute that participates with the MDPI Institutional Open Access Program

No Space Constraints, No Extra Space or Color Charges

No restriction on the maximum length of the papers, number of figures or colors

Coverage by Leading Indexing Services

Scopus, ESCI (Web of Science), Inspec, and other databases

Rapid Publication

A first decision is provided to authors approximately 23.4 days after submission; acceptance to publication is undertaken in 2.8 days (median values for papers published in this journal in the first half of 2025)

MDPI is a member of

Editorial Office
jlpea@mdpi.com

MDPI
Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
mdpi.com

July 2025